```
loop        bsr     process
            bsr     display
            bra     loop

process     bsr     getswitches
            sub.b   #20,D0
            bmi     low
            beq     special
            bra     return
low         clr.b   D0
return      rts

special     move.b  #255,D0
            rts

display     move.b  D0,lights
            rts

getswitches move.b  switch,D0
            rts
```

```
        cmp.l   #10,D0
        blt     silly
cont    ...



silly   move.l  #10,D0
        bra     cont
```

```
base.obj: base.asm
          as base.asm,base.lst,/li

base.cod: base.obj
          lyn base.obj;
          obsend base.cod,com,2,4800,n,8,1,srec;
```

```
for(x=0;x<10;x++) {
         stuff;
         }
```

```
         clr.l    D0
loop     (stuff)
         add.l    #1,D0
         cmp.l    #10,D0
         blt      loop
```

```
while(x==0) {
         stuff;
         }
```

```
l44      cmp.l    #0,D0
         bne      c7
         (stuff)
         bra      l44
c7       (rest of program)
```

Proposed Additions to the PDP-11 Instruction Set:

BBW	Branch Both Ways
BEW	Branch Either Way
BBBF	Branch on Bit Bucket Full
BH	Branch and Hang
BMR	Branch Multiple Registers
BOB	Branch On Bug
BPO	Branch on Power Off
BST	Backspace and Stretch Tape
CDS	Condense and Destroy System
CLBR	Clobber Register
CLBRI	Clobber Register Immediately
CM	Circulate Memory
CPPR	Crumple Printer Paper and Rip
CRN	Convert to Roman Numerals